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Inventor: **Fernando Gonzalez**

Title: **Methods of Forming Semiconductor Constructions**

Assignee: **Micron Technology, Inc.**

Serial No.: **10/659,596**

Filed: **September 9, 2003 [RCE Filed Herewith]**

INFORMATION DISCLOSURE STATEMENT

PURSUANT TO 37 C.F.R. §§ 1.56, 1.97 AND 1.98

The attached Form PTO-1449 is submitted in compliance with 37 CFR §1.56. Pursuant to FEDERAL REGISTER, Vol. 69, No. 182, pg. 56542 (September 21, 2004), no copies of any cited U.S. patents or U.S. published applications are included herewith.

No admission is made regarding whether the listed references are prior art.

Citation of these references is respectfully requested.

Respectfully submitted,

Date: 4/6/05

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Sheet 1 of 1

Form PTO-1400		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. MI22-2393		SERIAL NO. 10/659,596	
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT Fernando Gonzalez			
				FILING DATE Sept. 9, 2003 [RCE filed herewith]		GROUP 2818	
U.S. PATENT DOCUMENTS							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	6,251,754	06/01	Ohshima et al.			
	AB	6,245,161	06/01	Henley et al.			
	AC	5,374,581	12/94	Ichikawa et al.			
	AD	4,891,329	01/90	Reisman et al.			
	AE	6,150,031	11/00	Yonehara			
	AF	6,083,324	07/00	Henley et al.			
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation
							Yes No
	AN						
	AO						
	AP						
	AQ						
	AR						
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)							
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